

Notice of References Cited	Application/Control No. 10/602,503		Applicant(s)/Patent Under Reexamination GAZZOLA ET AL.	
	Examiner Li Liu		Art Unit 2613	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-6,466,886	10-2002	Marmur, Oren	702/126
*	C	US-7,050,463	05-2006	Cho et al.	370/476
*	D	US-6,684,033	01-2004	Doh et al.	398/202
*	E	US-5,982,837	11-1999	Earnest, Tim	375/377
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Sheyft et al: A 0.155-, 0.622-, and 2.488-Gb/s Automatic Bit-Rate Selecting Clock and Data Recovery IC for Bit-Rate Transparent SDH Systems, IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999, page 1935-1943.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.